# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Paul A. Farrar

CONDUCTIVE STRUCTURES IN INTEGRATED CIRCUITS

Docket No.: Filed:

Examiner:

303.557US1

March 1, 1999 Ginette Peralta Serial No.: 09/259,849

Due Date: November 8, 2005 (2 Month Date)

Group Art Unit: 2814

MS AF

Commissioner for Patents P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

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Receipt is hereby acknowledged for the thoughowing in the United States Patent and Trademark Office:

In re Patent Application of: Raul A. Farrar / Title: CONDUCTIVE STRUCTURES IN INTEGRATED CIRCUITS
Serial No.: 09/259,849 Filing Date: March 1, 1999

Information Disclosure Statement (2 pgs.), Form 1449 (1 pg.); A check in the amount of \$180.00 to cover the fee for consideration of Information Disclosure An Amendment and Response (23 Pages); A Supplemental Statement under 97(c); a Return Postcard and Party CONTENTS:

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### **EXPEDITED PROCEDURE - EXAMINING GROUP 2814**

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Applicant: Paul A. Farrar

Examiner: Ginette Peralta

**PATENT** 

Serial No.:

09/259,849

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March 1, 1999

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Title:

CONDUCTIVE STRUCTURES IN INTEGRATED CIRCUITS

## RESPONSE UNDER 37 C.F.R. § 1.116

MS AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

This paper responds to the Office Action mailed on September 8, 2005.